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Dunlop 13-6

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

## **Patent Application**

Applicant(s): Dunlop et al. 5

Case:

13-6

Serial No.:

09/716,977

Filing Date:

November 20, 2000

Group:

2634

10 Examiner: Eva Y. Zheng

Title:

Gated Clock Recovery Circuit

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AMENDMENT AFTER FINAL REJECTION UNDER 37 CFR §1.116

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In response to the outstanding final Office Action, dated July 30, 2004, please amend the above-identified patent application as follows.